

Attorney Docket No. 5649-1145

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Chang-Hyun Lee

Application Serial No.: not yet assigned

Filed: concurrently herewith

For: METHODS OF PROGRAMMING NON-VOLATILE SEMICONDUCTOR  
MEMORY DEVICES INCLUDING COUPLING VOLTAGES AND RELATED  
DEVICES

Date: August 13, 2003

MAIL STOP PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

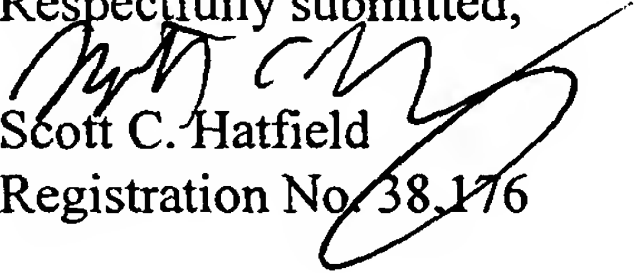
Sir:

Attached is a list of documents on Form PTO-1449, together with a copy of any listed foreign patent document and/or non-patent literature. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the waiver by the U.S. Patent and Trademark Office of requirements under 37 C.F.R. § 1.98(a)(2)(i) for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003.

It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97(b) and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

  
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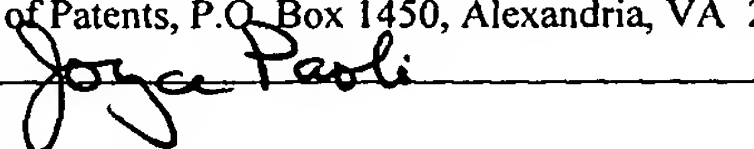
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Joyce Paoli



<b>FORM PTO-1449</b> U.S. Department of Commerce Patent and Trademark Office				Attorney Docket Number 5649-1145		Serial No. To Be Assigned	
LIST OF DOCUMENTS CITED BY APPLICANT  (Use several sheets if necessary)							
				Applicants: Chang-Hyun Lee			
				Filing Date: Concurrently Herewith		Group	
U. S. PATENTS & PATENT APPLICATION PUBLICATIONS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1.	5,677,873	10/14/97	Choi et al;	365	185.17	
	2.	5,715,194	02/03/98	Hu	365	185.17	
	3.	5,768,188	06/16/98	Park et al;	365	185.03	
	4.	5,991,202	11/23/99	Derhacobian et al;	365	185.19	
	5.	6,061,270	05/09/00	Choi	365	185.02	
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes   No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	6.	Jae-Duk Lee; "Effects of Floating-Gate Interference on NAND Flash Memory Cell Operation", IEEE Electron Device Letters, Vol. 23, No. 5 (May 2002).					